



Robert Greene Sterne
Jorge A. Goldstein
David K.S. Cornwell
Robert W. Esmond
Tracy-Gene G. Durkin
Michele A. Cimbala
Michael B. Ray
Robert E. Sokohl
Eric K. Steffe
Michael Q. Lee
John M. Covert
Robert C. Millonig
Donald J. Featherstone
Timothy J. Shea, Jr.
Michael V. Messinger
Judith U. Kim
Jeffrey T. Helvey
Eldora L. Ellison
Donald R. Banowitz

Peter A. Jackman
Brian J. Del Buono
Mark Fox Evens
Vincent L. Capuano
Elizabeth J. Haanes
Michael D. Specht
Kevin W. McCabe
Glenn J. Perry
Edward W. Yee
Grant E. Reed
Virgil Lee Beaton
Theodore A. Wood
Joseph S. Ostroff
Jason D. Eisenberg
Tracy L. Muller
Jon E. Wright
LuAnne M. DeSantis
Ann E. Summerfield
Helene C. Carlson

Cynthia M. Bouchez
Timothy A. Doyle
Gaby L. Longworth
Lori A. Gordon
Laura A. Vogel
Bryan L. Wade
Bashir M.S. Ali
Shannon A. Carroll
Anbar F. Khal
Michelle K. Holoubek
Marsha A. Rose
Scott A. Schaller
Lei Zhou
Young Tang
Christopher J. Walsh
W. Blake Coblentz
James J. Pohl
John T. Haran
Mark W. Rygiel

Michael R. Malek*
Carla Ji-Eun Kim
Doyle A. Siever*
Ulrike Winkler
Bryan L. Skelton*
Paul A. Calvo
Robert A. Schwartzman
C. Matthew Rozier
Alexandra K. Pechhold

Registered Patent Agents*
Karen R. Markowicz
Matthew J. Dowd
Katrina Yujian Pei Quach
Julie A. Heider
Mita Mukherjee

Scott M. Woodhouse
Peter A. Socarras
Jeffrey K. Mills
Danielle L. Letting
Lori Brandes
Steven C. Oppenheimer
Aaron S. Lukas
Gaurav Asthana

Of Counsel
Edward J. Kessler
Kenneth C. Bass III
Marvin C. Guthrie
Christopher P. Wrist

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WRITER'S DIRECT NUMBER:
(202) 772-8677
INTERNET ADDRESS:
RSOKOHL@SKGF.COM

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Art Unit 2819

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Re: U.S. Utility Patent Application
Appl. No. 10/625,702; Filed: July 24, 2003
For: **Subranging Analog to Digital Converter with Multi-Phase Clock
Timing**
Inventors: VAN DER GOES *et al.*
Our Ref: 1875.2820002

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Reply Under 37 C.F.R. § 1.116;
2. List of cited references on Form PTO/SB/08A (1 sheet); and
3. One (1) return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Robert Sokohl
Attorney for Applicants
Registration No. 36,013

RES/MRM:bac
Enclosures

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